

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10/596,944
	Filing Date		6/29/2006
	First Named Inventor	Robert L. Maziasz	
	Art Unit	2825	
	Examiner Name	Patrick Sandoval	
	Attorney Docket Number	SC12656TS	

## U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6434721	B1	2002-08-13	Chiluvuri, et al.	
	2	6446239	B1	2002-09-03	Ara Markosian, et al.	
	3	6253351	B1	2001-06-26	Masahiro Fukui, et al.	
	4	5416722		1995-05-16	Lawrence B. Edwards	
	5	6526555	B1	2003-02-25	Steven Tieg, et al.	

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## U.S.PATENT APPLICATION PUBLICATIONS

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	1	2002041587	JP		2002-02-08	Fujimoto Shinya		<input type="checkbox"/>

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	1	HYUNCHUI SHIN AND CHI-YUAN LO, An Efficient Two-Dimensional Layout Compaction Algorithm, 26th ACM/IEEE Design Automation Conference, 1989.	<input type="checkbox"/>
	2	WAYNE H. WOLF, ROBERT G. MATHEWS, JOHN A. NEWKIRK, and ROBERT W. Dutton, Algorithms for Optimizing, Two-Dimensional Symbolic Layout Compaction, IEEE Transactions on Computer-Aided Design, Volume 7, No. 4, April 1988, pp 451-466.	<input type="checkbox"/>

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Examiner Signature	/Patrick Sandoval/	Date Considered	08/05/2008
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